DC/DC Converter CNN2-XXSXXA3NT Series



Typical Feature

- ◆ Fixed Input Voltage, isolated & Unregulated Single Output power 2W
- ◆ Continuous short circuit protection
- ◆ Operating Temperature: -40°C to +105°C
- ◆ Small SMD package, International standard pin-out
- ◆ Isolation Voltage 3000VDC
- ◆ High efficiency up to 86%
- ◆ No load input current as low as 5mA
- ◆ ESD meet Contact 8KV





Application Filed

CNN2-XXSXXA3NT is suitable for pure digital systems, low frequency analog circuits, relay-driven circuits. It is specially designed for applications where an isolated voltage is required in a distributed power supply system. It could be widely used in the below products:

- 1. The voltage of the input power supply is relatively stable (voltage change range:±10%Vin)
- 2. Isolation between input and output is required (Isolation Voltage≤3000VDC);
- 3. Low requirements for output voltage stability and output ripple noise;

Typical Product List						
Part No	Input Voltage (VDC)	Output Voltage/Current Voltage Current		Max. Capacitive Load(Max)	Ripple & Noise 20MHz (Typ/Max)	Efficiency (Min/Typ)
	Range	(VDC)	(mA) Max / Min	u F	mVp-p	%
CNN2-3V3S05A3NT	3.3 (2.97-3.63)	5	400/40	2400	50/100	79/82
CNN2-05S3V3A3NT	5 (4.5-5.5)	3.3	400/40	2400	50/100	77/80
CNN2-05S05A3NT		5	400/40	2400	50/100	80/83
CNN2-05S09A3NT		9	222/22	1000	80/100	82/85
CNN2-05S12A3NT		12	167/17	1000	80/100	83/86
CNN2-05S15A3NT		15	133/13	560	80/100	79/82
CNN2-12S05A3NT		5	400/40	2400	80/100	81/84
CNN2-12S12A3NT	12 (10.8-13.2)	12	167/17	560	80/100	83/86
CNN2-12S15A3NT		15	133/13	560	80/100	81/84
CNN2-12S24A3NT		24	83/8	470	80/100	81/84
CNN2-15S05A3NT	(13.5 - 16.50	5	400/40	2400	80/100	79/82
CNN2-15S12A3NT		12	167/17	560	80/100	81/84
CNN2-15S15A3NT		15	133/13	560	80/100	81/84

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CNN2-24S05A3NT	24 (21.6-26.4)	5	400/40	2400	80/100	81/84
CNN2-24S09A3NT		9	222/22	1200	80/100	82/85
CNN2-24S12A3NT		12	167/17	1200	80/100	83/86
CNN2-24S24A3NT		24	83/8	470	80/150	81/84

Note 1: The typical output efficiency is based on that product is full loaded and burned-in after half an hour.

Note 3: Ripple & Noise Tested by twisted-pair method, for details please check Ripple & Noise Test Method.

put Specifications						
Item	Operatin	Operating Condition		Тур.	Max.	Unit
	3.3Vdc Input	3.3Vdc output	-	758/10	777/15	_
		5Vdc/ 9Vdc output	-	739/20	758/25	
		12Vdc output	-	722/30	739/35	
		24Vdc output	-	758/40	777/50	
		3.3Vdc output	-	500/5	513/12	
		5Vdc output	-	476/5	488/12	mA
	5Vdc output	9Vdc output	-	465/10	476/20	
		12Vdc output	-	455/20	465/30	
		24Vdc output	-	488/30	500/40	
Input Current (Full	12Vdc Input	5Vdc output	-	200/8	235/15	
load/No load)		12Vdc output	-	190/8	235/15	
		15Vdc output	-	192/12	235/18	
		24Vdc output	-	185/10	235/15	
	15Vdc output	5Vdc output	-	160/10	180/18	
		12Vdc output	-	158/10	170/18	
		15Vdc output	-	156/10	170/18	
	24Vdc input	5Vdc output	-	100/8	120/15	
		9Vdc output	-	100/8	120/15	
		12Vdc output	-	98/8	120/15	
		24Vdc output	-	96/8	120/15	
Reflected Ripple Current	-		-	15	-	
Overshoot Voltage	3.3Vdc Input		-0.7	-	9	VDC
	5Vdc Input		-0.7	-	11	
	12Vdc Input		-0.7	-	18	
	24Vdc Input		-0.7	-	30	

Note 2: The fluctuation range of full load efficiency(%, TYP) is $\pm 2\%$, full load output efficiency= total output power/module's input power.

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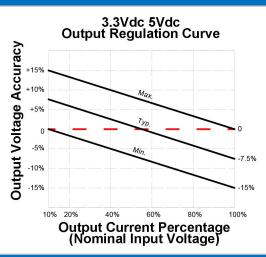


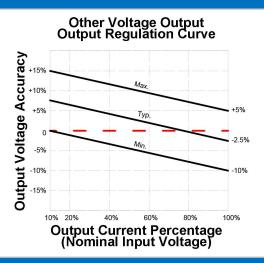
Overshoot Current		_	_	0.8		Α	
			-		-	<u> </u>	
Input Filter Type	-			Capacitor Filter Unavailable			
Hot Plug				Unava	ilable		
Output Specifications	S					_	
Item	Operating	g Condition	Min.	Тур.	Max.	Unit	
Output Voltage Accuracy	-			See Error Env	elope Curve		
Lino Deculation	Input voltage	3.3Vdc/5Vdc output	-	-	1.5		
Line Regulation	change ±1%	Other voltage output	-	-	1.2		
Load Demilation	100/ 1000/ 15-5	3.3Vdc/5Vdc output	-	15	20	%	
Load Regulation	10%-100% load	Other voltage output	-	10	15		
Temperature Drift Coefficient	Full load		-	-	±0.03	%/°C	
Short Circuit Protection	-			Continuous, S	Continuous, Self-recovery		
General Specification	าร						
Item	Operating Condition		Min.	Тур.	Max.	Unit	
Insulation Withstand Voltage	Input-output, Test 1min, leakage current≤0.5mA		3000	-	-	VDC	
Insulation Resistance	Input-output, Insulation Voltage 500VDC		1000	-	-	ΜΩ	
Isolation Capacitor	Input-output, 100KHz/0.1V		-	20	-	PF	
Operating Temperature	Temperature≥105°C,see Temperature Derating Curve		-40	-	105		
Case Rising Temperature	Test Environment Temperature 25°C		-	15	-	$^{\circ}$	
Storage Temperature	-		-55	-	135		
Reflow Temperature	Peak Value Temperature ≤ 250°		℃, the maximum time is 60s for temp over 217℃				
Storage Humidity	No condensing		-	-	95	%RH	
Switching Frequency	Full load	3.3Vdc/5Vdc Input	-	260	-	KHz	
		12Vdc/24Vdc Input	-	450	-		
MTBF	MIL-HDBK-217F@25℃		3000			Khours	
laterial Characterist	ics						
Case Mate	erial	Black flan	ne-retardant he	at-resistant plas	stic (UL94 V-0)		
Packing Dimension			12.7X11	.20X7.25 mm			
Product Weight	SMD Package	1.4g (TYP.)					
Cooling Method		Natural air cooling					



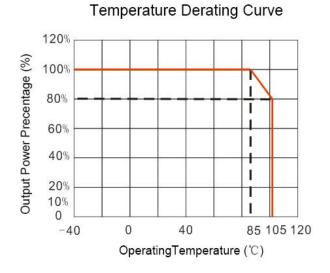
EMC Characteristic					
EMI	CE	CISPR32/EN55032 CLASS B (EMC Recommended Circuit)			
EIVII	RE	CISPR32/EN55032 CLASS B (EMC Recommended Circuit)			
EMS	ESD	IEC/EN61000-4-2 Air ±8kV, Contact ±8kV perf. Criteria B			

Output Voltage Regulation Curve





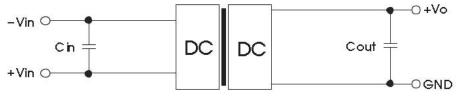
Product Character Curve



Application Circuit

1. Typical Application

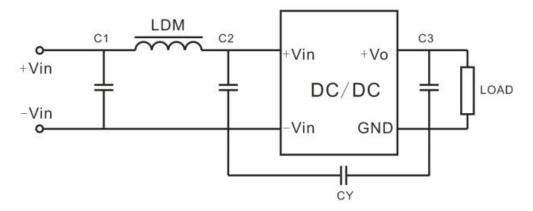
In order to ensure the input/output ripple and noise decreased, capacitor filter net could be connected to input and output side, application circuit as below photo 3; choosing suitable filter capacitor is very important, start-up problems may be caused by too large capacitance.



Note 1: Cin is 4.7uF/50V, Cout is 10uF/50V



2. EMC Typical Recommended Circuit



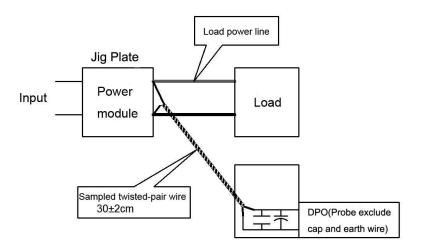
Recommended EMC Circuit

Note 2:C1,C2 is 4.7uF/50V, LDM is 6.8uH, CY is 1nF/250Vac, for C3, please refer to the Typical Circuit.

3. Ripple& Noise Test (Twisted Pair Method 20MHZ bandwidth)

- 1).12# twisted pair to connect, Oscilloscope bandwidth set as 20MHz, 100M bandwidth probe, terminated with 0.1uF polypropylene capacitor and 4.7uF high frequency low resistance electrolytic capacitor in parallel, oscilloscope set as Sample pattern.
- 2). Ripple& Noise Test Method:

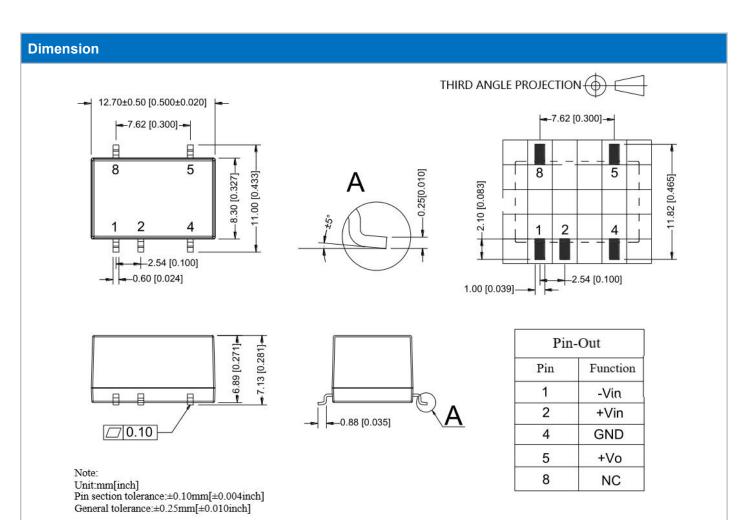
Input terminal connect to power supply, output terminal connect to electronic load through jig plate. Use 30cm±2 cm sampling line. Power line selected from corresponding diameter wire with insulation according to the flow of output current.



4. Output Load Requirement

In order to ensure the converter can work reliably with high efficiency, the minimum load should not less than 10% rated load when it is used. If the needed power is indeed small, please parallel a resistor at the output side. (The actual using power and the power of the resistor should be more than 10% rated power)





NC pin:do not connect to any external circuit

Note: if the definition of pin is not in accordance with the model selection manual, please refer to the label on actual item.

