CNNV25-XXSXXA3NT Series

DC/DC Converter



Typical Feature

- ◆ Fixed Input Voltage, isolated & regulated output, output power 0.25W
- ◆ Continuous short circuit protection
- ◆ Operating Temperature: -50°C to +105°C
- ◆ Small SMD package, international standard pin out
- ◆ Isolation Voltage 3000VDC
- ◆ High efficiency up to 82%
- ◆ No-load input current as low as 5mA





Application Filed

CNNV25-XXSXXA3NT products are suitable for pure digital circuits, general low-frequency analog circuits, relay drive circuits, etc. They are specially designed for applications where a set of voltages isolated from the input power supply need to be generated in the on-board power supply system.

This product is suitable for:

- 1. The input power supply voltage is relatively stable (voltage variation range $\pm 10\% Vin$);
- 2. Isolation is required between input and output (isolation voltage ≤3000VDC);
- 3. The requirements for output voltage stability and output ripple noise are not high.

Typical Product List

71						
Part No	Input Voltage	Input Specification		Max capacitive	Ripple& Noise	Efficiency
	Range	Voltage	Current	load	(Typ./Max.)	(Min./Typ.)
	(VDC)	(VDC)	(mA) Max./Min.	uF	mVp-p	%
CNNV25-05S05A3NT	5 (4.5-5.5)	5	50/5	2400	50/100	80/82

Note 1: The typical value of output efficiency is based on the product being aged at full load for half an hour.

Note 2: The full load efficiency fluctuation range in the table is $\pm 2\%$. The full load output efficiency is equal to the total output power divided by the input power of the power module.

Note 3: The ripple and noise test method uses the twisted pair test method. For specific test methods and matching, please refer to the following (Ripple & Noise Test Instructions).

Input Specification

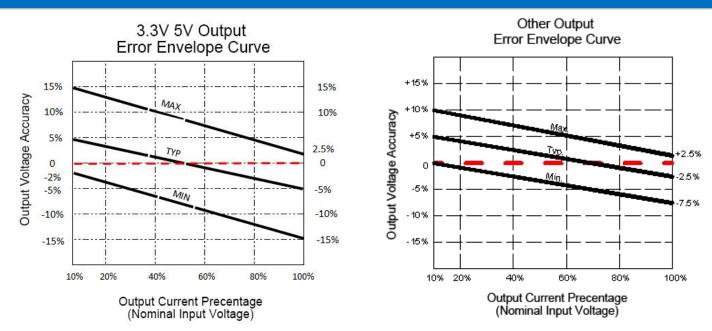
Item	Operating Condition	Min.	Тур.	Max.	Unit	
Input current (Full Load / No Load)	5Vdc Input	-	56/5	64/10	mA	
Reflected ripple current	-	-	15	-		
Impulse voltage	5Vdc Input	-0.7	-	9	VDC	
Impulse current	-	-	0.8	-	А	
Input filter type		Capacitor Filter				
Hot plug		Unavailable				



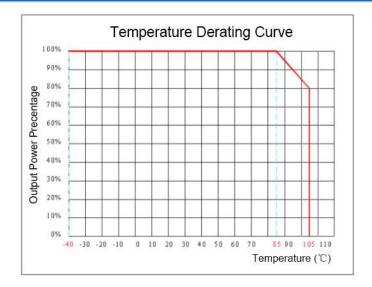
Output Sp	ecification									
lte	em	Operating Cond		1	Min.	Тур.	М	ax.	Unit	
Output Volta	age Accuracy	Nominal input, full load			See Error Envelope Curve					
Line Re	gulation	Input voltage change ±1% -			-	-	±1.5		%	
Load Re	egulation	10%-	10%-100% load -		10	1	5	%		
-	ature Drift ficient	100	100% load -		-	±0	0.03	%/℃		
Short Circu	it Protection		Continuous, Self-re			covery	·			
General S	pecification	1								
lte	em		Operating Condition			Min.	Тур.	Max.	Unit	
	Withstand tage	Input-output, test	put, test for 1 minute, leakage current is less than 0.5mA		3000	-	-	VDC		
Insulation	Resistance	Input-ou	Input-output, insulation voltage 500VDC			1000	-	-	ΜΩ	
Isolation	Capacitor	In	Input-output, 100KHz/0.1V		-	20	-	PF		
Operating 1	Temperature	Temper	Temperature ≥105°C, use at derating (see derating curve)		-50	-	115			
Case Temp	erature Rise	Test e	Test environment temperature 25°C		25 ℃	-	15	-	$^{\circ}$	
Storage Te	emperature		-			-55	_	135		
Reflow Te	emperature		Peak temper	rature Tc≤2	250℃, the maximu	ım time abo	ve 217℃ i	s 60S		
Storage Humidity No co		No conde	ondensation		-	-	95	%RH		
Switching	Frequency Full load		Full load @ 5	d @ 5Vdc input		-	360	-	KHz	
MTBF MIL-HDE		MIL-HDBK-2	BK-217F@25℃		3000			K hours		
Physical (Characteris	tic								
Case Material				Black flame retardant and heat resistant epoxy resin (UL94V-0)						
Dimension SMD Weight		SMD Packag	12.7X11.20X7.25 mm							
		OND T donag	- 1		1.4g (Typ.)					
	Cooling	Method			Na	itural air cod	oling			
EMC Char	racteristic									
CE EMI		CISP	CISPR32/EN55032 CLASS B (EMC Recommended Circuit)							
	RE		CISPR32/EN55032 CLASS B (EMC Recommended Circuit)							
EMS	ESI	D IEC/E	EN61000-4-2	Air ± 8k\	V, Contact ± 8kV	perf.Criter	ria B			



Output Voltage Error Envelope Curve



Products Characteristic Curve

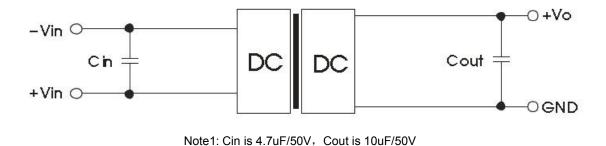


Application Circuit

1. Typical Applications

If you need to further reduce the input and output ripples, you can connect a capacitor filter network at the input and output ends. The application circuit is shown in the figure below.

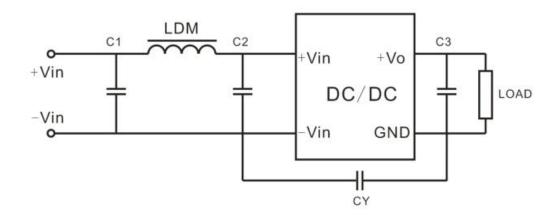
But you should pay attention to choosing a suitable filter capacitor. If the capacitor is too large, it is likely to cause startup problems.



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2. EMC Recommended Circuit

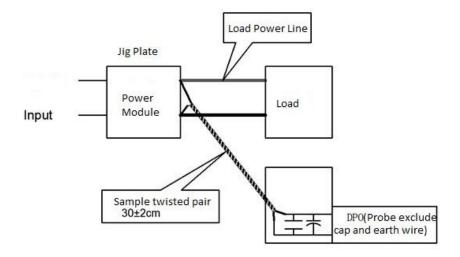


EMC Recommended Circuit

Note 2: C1, C2 are 4.7uF/50V, LDM is 6.8uH, CY is 270pF/3000VDC, C3 can refer to the typical circuit

3. Ripple & Noise Test Instructions (Twisted Pair Method 20MHz Bandwidth)

- 1) Ripple noise is connected using 12# twisted pair, the oscilloscope bandwidth is set to 20MHz, 100M bandwidth probe, and 0.1uF polypropylene capacitor and 4.7uF high-frequency low-resistance electrolytic capacitor are connected in parallel on the probe end. The oscilloscope sampling uses the Sample sampling mode.
- 2) Output ripple noise test diagram: Connect the power input to the input power supply, and the power output is connected to the electronic load through the fixture board. The test uses a $30\text{cm}\pm2$ cm sampling line to directly sample from the power output port. The power line selects the corresponding wire diameter with insulated wire according to the output current.



4. Output Load Requirement

To ensure that the module can work efficiently and reliably, its minimum output load cannot be less than 10% of the rated load when in use. If the power you need is indeed small, please connect a resistor in parallel to the output end (the sum of the power consumed by the resistor and the actual power used is greater than or equal to 10% of the rated power).

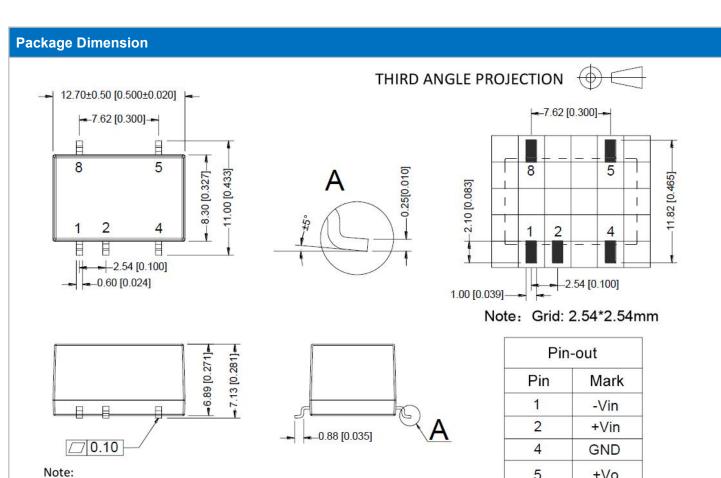
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Unit:mm[inch]

Pin section tolerances: ± 0.10 mm[± 0.004 inch]

General tolerances: ± 0.25 mm[± 0.010 inch]





NC: Pin to be isolated from circuitry

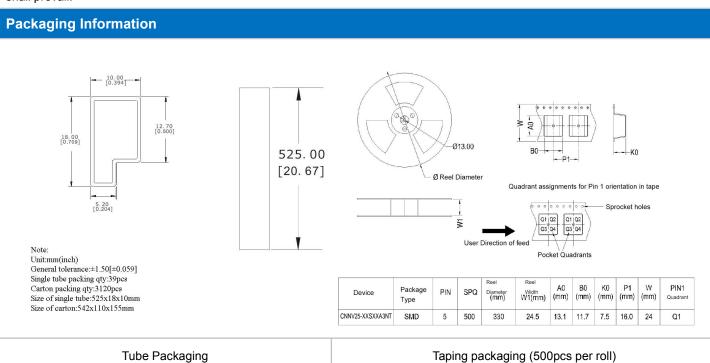
+Vo

NC

5

8

Note: If the pin definitions of the power module are inconsistent with those in the selection manual, the markings on the actual label shall prevail.



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Note:

- 1. If the product works below the minimum required load, it cannot be guaranteed that the product performance meets all performance indicators in this manual;
- 2. The maximum capacitive load is tested within the input voltage range and full load conditions;
- 3. Unless otherwise specified, all indicators in this manual are measured at Ta=25°C, humidity<75%RH, nominal input voltage and output rated load;
- 4. All indicator test methods in this manual are based on our company's corporate standards;
- 5. Our company can provide product customization. For specific details, please contact our technical staff directly.