# DC/DC Converter **CNWV75-XXSXXANT Series**



# **Typical Feature**

- ◆ Fixed Input Voltage, isolated & regulated Output, power 0.75W
- ◆ High efficiency up to 75%
- ◆ Small SMD package, international standard pin out
- ◆ Isolation Voltage 1500VDC
- ◆Operating Temperature: -40°C to +85°C
- ◆ Plastic case, meet to UL94 V-0 standard



# **Application Filed**

Widely used in instrumentation, communication, pure digital circuits, general low-frequency analog circuits, relay drive circuits, data exchange circuits, etc.

<b>Typica</b>		

Part No	Input vo	•	Input Voltage/Current(Vo/Io)		Input Current(mA) Nominal voltage		Max capa citive load	Ripple & Noise Max	Efficiency (%)@full load, nominal input voltage	
	Rated	Range	Vo (VDC)	lo (mA) Max / Min	Full load	No Load	uF (Max)	Max/Typ	Min	Тур
CNWV75-3V3S3V3ANT	3.3	3.135 - 3.465	3.3	200/20	290	8	2400	80/50	67	70
CNWV75-05S3V3ANT			3.3	200/20	200	6	2400	80	67	70
CNWV75-05S05ANT	5	4.75 -	5	150/15	205	6	2400	80	70	73
CNWV75-05S12ANT		5.25	12	62/7	186	8	560	80	72	75
CNWV75-12S3V3ANT		11.4	3.3	200/20	86	8	2400	80	67	70
CNWV75-12S05ANT	12	-	5	150/15	83	8	2400	80	70	73
CNWV75-12S12ANT		12.6	12	62/7	83	8	560	80	72	75
CNWV75-24S3V3ANT		22.8	3.3	200/20	45	8	2400	80	67	70
CNWV75-24S05ANT	24	-	5	150/15	41	8	2400	80	70	73
CNWV75-24S12ANT		25.2	12	62/7	41	8	560	80	72	75

In order to ensure that the module can work efficiently and reliably, when in use, the minimum output load cannot be less than 10% of the rated load. If the power you need is really small, please connect a resistor in parallel at the output end, the recommended resistance is equivalent to 10% of the rated power.

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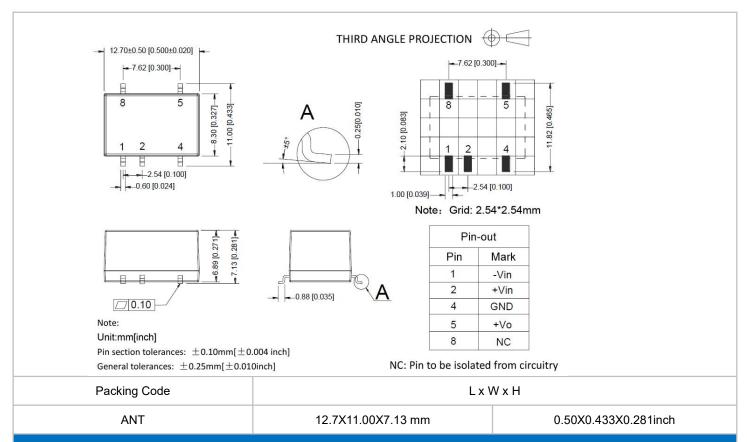
Item	Operating Condition	Min.	Тур.	Max.	Unit
Input Oversheet Veltage	5Vdc Input	-0.7		9	VDC
Input Overshoot Voltage	12Vdc Input	-0.7		18	VDC

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			24Vdc Input	-0.7		30		
Input Fi	ilter Type				Capaci	tor Filter	1	
Output Sp	pecifications							
lt	em	0	perating Condition	Min.	Тур.	Max.	Unit	
Outpu	t Power			0.07	-	0.75	W	
Output Volta	age Accuracy	No	ominal input, full load	-	±2	±3		
Load R	egulation		10%-100% load	-		±3	%	
Line Re	egulation	Inpu	it voltage change ±1%	-	-	±0.25		
Ripple &	Noise ①	Nomin	al input, full load, 20MHZ bandwidth		35	80	mVp-p	
•	ature Drift fficient		100% load	-	-	±0.03	<b>%/°</b> C	
Short Circu	uit Protection			Continuous, Self-r	ecovery			
Note: ① rip	pple & noise is to	ested by Twi	sted pair method.					
General S	pecifications	S						
Switching Frequency			Typical			260KHz (Typ.)		
Operating Temperature			see Temperature Deratir		-40℃ ~+85℃			
Storage T	emperature		-55°C ~ +125°			2		
Reflow Te	emperature		Peak temperature Tc≤250°C, maxi time is 60S for temperature above 217°C					
Case Temp	perature Rise		Within temperature derat	ing curve		25°C(Typ.)		
Relative	Humidity		non-condensing			5%~95%		
Case	Material				Black flame	-retardant, heat-r	esistant plastic	
						(UL94 V-0)		
	g temperature	10 s	seconds at a distance of 1.5mm from case			300°C MAX		
	n Voltage		Test 1min, leakage currer			1500Vdc		
	Capacitor		Input-output, 100KHz			20 pF (Typ.)		
MTBF MIL-HDBK-217F(			MIL-HDBK-217F@2	<b>25℃</b>				
Product Weight 1.4g (Typ.)								
EMC Char	racteristic							
CE C			CISPR32/EN55032 CLASS B(see EMC recommended circuit)					
RE			CISPR32/EN55032 CLAS	SS B(see EMC recor	mmended circui	t)		
EMS	ESC	)	IEC/EN61000-4-2 Air±	8kV, Contact±6kV	perf.Criteria B			
Packing In	nformation							



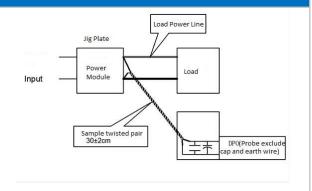


# Ripple& Noise Test: (Twisted Pair Method 20MHZ bandwidth)

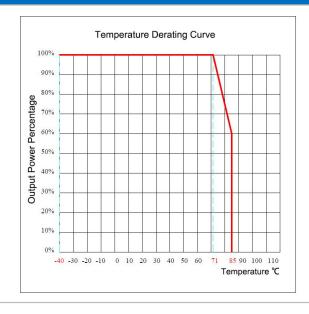
#### Test Method:

a.12# twisted pair to connect, Oscilloscope bandwidth set as 20MHz, 100M bandwidth probe, terminated with 0.1uF polypropylene capacitor and 10uF high frequency low resistance electrolytic capacitor in parallel, oscilloscope set as Sample pattern.

b. Input terminal connect to power supply, output terminal connect to electronic load through jig plate, Use 30cm±2 cm sampling line, Power line selected from corresponding diameter wire with insulation according to the flow of output current.



#### **Products Characteristic Curve**





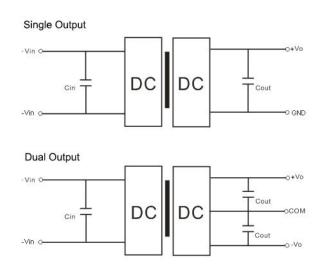
# **Application Circuit**

#### 1. Output load requirements

- a. In order to ensure the converter can work reliably with high efficiency, the minimum load should not less than 10% rated load when it is used. If the needed power is indeed small, please parallel a resistor at the output side, the resistance equal to 10% nominal load.
- b. The maximum capacitive load is tested under nominal input full load, and cannot exceed the maximum capacitive load of output terminal under operation, otherwise it will cause it difficult to start up and damage the product.

#### 2. Recommended circuit

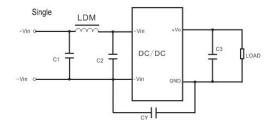
In order to ensure the input/output ripple and noise decreased, capacitor filter net could be connected to input and output terminal, application circuit as below photo 1; choosing suitable filter capacitor is very important, start-up problems may be caused by too large capacitance. To ensure the modules running safely and reliably, the recommended capacitive load values as shown in Table 1.



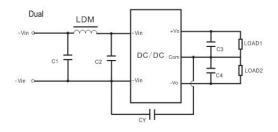
#### Recommended capacitive load value(Table 1)

Vin (Vdc)	Cin	Single Vout Vdc	Cout (µF)	Dual Vout (Vdc)	Cout (μF)
5	10 µF/16V	3. 3	10 µF/16V	±3.3	4, 7 µ F/16V
12	2. 2 µ F/25V	5	10 µ F/16V	±5	4. 7 μ F / 16V
15	2. 2 µ F/25V	9	2, 2 µ F/25V	±9	2. 2 µF/25V
24	1 µ F/50V	12	2. 2 µF/25V	±12	1 µ F/25V
		15	1μF/25V	±15	1μF/16V
		24	1μF/50V	±24	0. 47 μF/50

#### 3. EMC recommended circuit

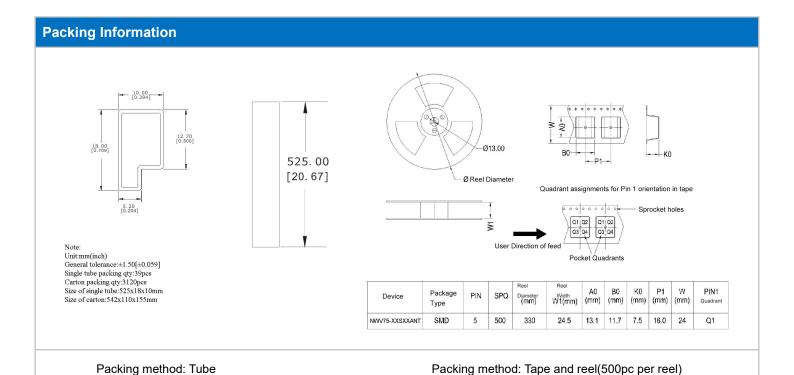


Input \	/oltage	5VDC	12/15/24VDC
	C1/C2	4.7 µF/16V	4. 7μF/50V
	CY	270pF/6kV	270pF/6kV
EMI	СЗ	Refer to Cout specs according to Table	
	LDM	6.8 µ H	6.8 µ H



Input	Voltage	5VDC	12/15/24VDC
	C1/C2	4.7 µ F/16V	4. 7 μ F/50V
	CY	270pF/6kVdc	270pF/6kVdc
EMI	C3/C4	Refer to Cout spece	s according to Table 1
	LDM	6.8 µ H	6.8µH





# Note:

- 1. If the product is operated under the min. required load, the product performance cannot be guaranteed to comply with all performance indexes in this datasheet;
- 2. The maximum capacitive load is tested under nominal input voltage range and full load condition;
- 3. Unless otherwise specified, data in this datasheet are tested under conditions of **Ta=25**℃, **humidity<75%** when inputting nominal voltage and outputting rated load(pure resistance load);
- 4. All index testing methods in this datasheet are based on our Company's corporate standards.
- 5. We can provide customized product service;



#### **CONTRA-Solution GmbH**

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